

Multi-Port Test Report

August 11, 2014 8:49 PM

Ports Tested: 24

PSE Type: 24-Port 2-Event PSE

PD Emulation Characteristics

PD Type(s) 1&2 Type-1 and Type-2 PD's Emulated

PM Method PHY LLDP Never Used

System Type PSA Test Ports are PSA-3102

Evaluation Categories

Nominal or Ideal Result

Non-Ideal Feature / Design Limitation

802.3at Specification Violation



PSE Multiport Performance Report

Multi-Port Suite 4.1.00

Report Version 4.1.00

Multi-Port Test Resources	1-1	1-2	2-1	2-2	3-1	3-2	4-1	4-2	5-1	5-2	6-1	6-2	7-1	7-2	8-1	8-2	9-1	9-2	10-1	10-2	11-1	11-2	12-1	12-2	
192.168.221.103																									

mp class admit					Port Admittance Test																							
PD Class	Powered	Granted	Flapping	Inactive	Inactive Ports												Flapping Ports						Ungranted Ports (LLDP)					
0	8		15	16	192.168.221.103: 4-2 5-1 5-2 6-1 6-2 7-1 7-2 8-1 8-2 9-1 10-1 10-2 11-1 11-2 12-1 12-2												192.168.221.103: 3-2 4-1 4-2 5-1 5-2 6-1 6-2 7-1 7-2 8-1 8-2 9-1 9-2 10-2 12-2											
1	24		6	0	192.168.221.103: NONE												192.168.221.103: 1-2 5-2 6-2 8-2 10-1 12-2											
2	24		6	0	192.168.221.103: NONE												192.168.221.103: 5-1 5-2 9-2 10-2 12-1 12-2											
3	14		10	10	192.168.221.103: 8-1 8-2 9-1 9-2 10-1 10-2 11-1 11-2 12-1 12-2												192.168.221.103: 8-1 8-2 9-1 9-2 10-1 10-2 11-1 11-2 12-1 12-2											
4	8		9	16	192.168.221.103: 4-2 5-1 5-2 6-1 6-2 7-1 7-2 8-1 8-2 9-2 10-1 10-2 11-1 11-2 12-1 12-2												192.168.221.103: 4-2 5-1 5-2 6-1 6-2 7-1 7-2 8-1 8-2											

mp pwrup time								Group Power-Up Timing Test		
PD Class	Fast_PwrUp	Slow_PwrUp	Avg_PwrUp	Fast_LLDP	Slow_LLDP	Avg_LLDP				
2	1.2	11.7	6.4				seconds			
4	0.5	7.2	3.9				seconds			

First Powered Port 1		Unpowered Ports 1	
192.168.221.103: 1-1		NONE	

Final Powered Port 1		Unpowered Ports 2	
192.168.221.103: 6-2		NONE	

First Powered Port 2		Unpowered Ports 2	
192.168.221.103: 1-1		NONE	

Final Powered Port 2		Unpowered Ports 1	
192.168.221.103: 4-2			

First Granted Port 1		Ungranted Ports 1	

Final Granted Port 1		Ungranted Ports 2	

First Granted Port 2		Ungranted Ports 2	

Final Granted Port 2		Ungranted Ports 2	

mp static cap					Static Power Capacity Test		
Static Capacity 1:	230.8 W	Under-Alloc. Pwr. 1	114.0 W				
Static Capacity 2:	233.6 W	Under-Alloc. Pwr. 2	0.0 W				

PD Class	PD Type	Pclass_PSE	Min_PD's	Alloc_Pwr/PD
0	1	14.3 W	16	28.9 W
4	2	29.0 W	8	29.2 W

Static Cap Port Count 1	Static Cap Port Count 2
7	7

Out_of_Service_Ports: NONE

mp trans cap					Transient Reserve Power Test			
PD Type	Transient/port	Reserve@Full	%_Reserve	Reserve@Half				
1	399.0 mA	0.3 W	102.0 %	0.0 W				
2	598.0 mA	0.0 W	0.7 %	0.0 W				

Out_of_Service_Ports: NONE

mp port caps										Static Power Uncertainty Test		
PD Class	Max_Pwr/port	Min_Pwr/port	Avg_Pwr/port	Uncertainty/port	Variation	Premature Dropped Ports						
0	35.0	15.4	34.4	19.0 W	56.9 %	192.168.221.103: 4-2						
1	5.6	4.0	5.5	1.5 W	28.9 %	192.168.221.103: 4-2						
2	9.8	9.2	9.5	0.0 W	6.3 %	NONE						
3	20.6	19.7	20.1	3.6 W	4.5 %	NONE						
4	34.8	26.7	34.2	7.5 W	23.7 %	192.168.221.103: 4-2						

mp overld time					Overload Shutdown and Restore Timing			
Operation	Ovld_Level	Minimum	Maximum	Average				
Shutdown	188 mA	66.5	68.7	67.6	msec			
Recycle		1.7	1.7	1.7	seconds			

First Port Down:		Stuck_ON Ports	
192.168.221.103: 8-2		NONE	

Last Port Down:		Out_of_Service Ports	
192.168.221.103: 1-2		NONE	

mp admit cases							Multi-Port Power-Up Port Groups					
Parameter	Case 1	Case 2	Case 3	Case 4	Case 5	Case 7						
Expected Ports	12	8	12	14	8	12	8					
Actual Ports Powered	12	8	12	14	8	12	7					

mp cap stress				Multi-Port Stress Test	
Test Conditions	Test Duration	PD Class	Actual Load Power		
Dropped Power Count:	2 minutes	4	222.1 watts		
Power Drop Ports	12				
Out_of_Service Ports	192.168.221.103: 4-2				
Out_of_Service Ports	NONE				

